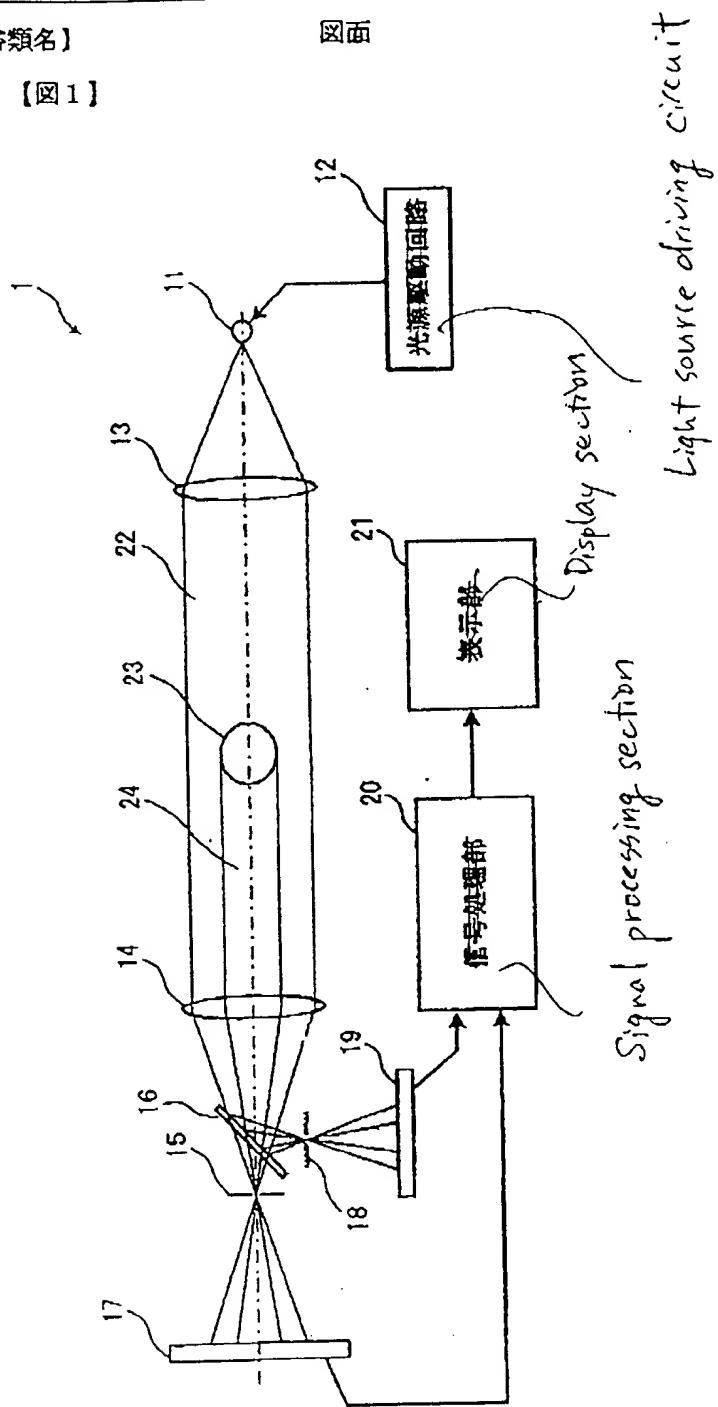


【審査名】

図面

【図1】

Fig. 1

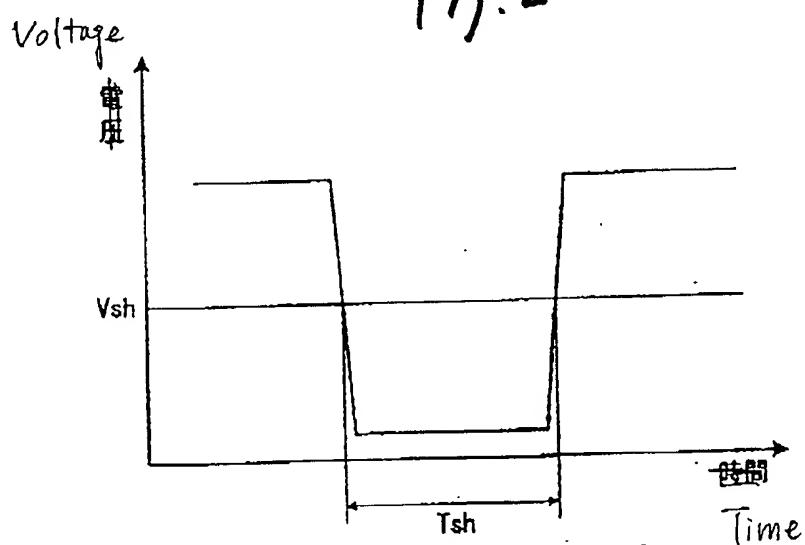


Signal processing section

Display section

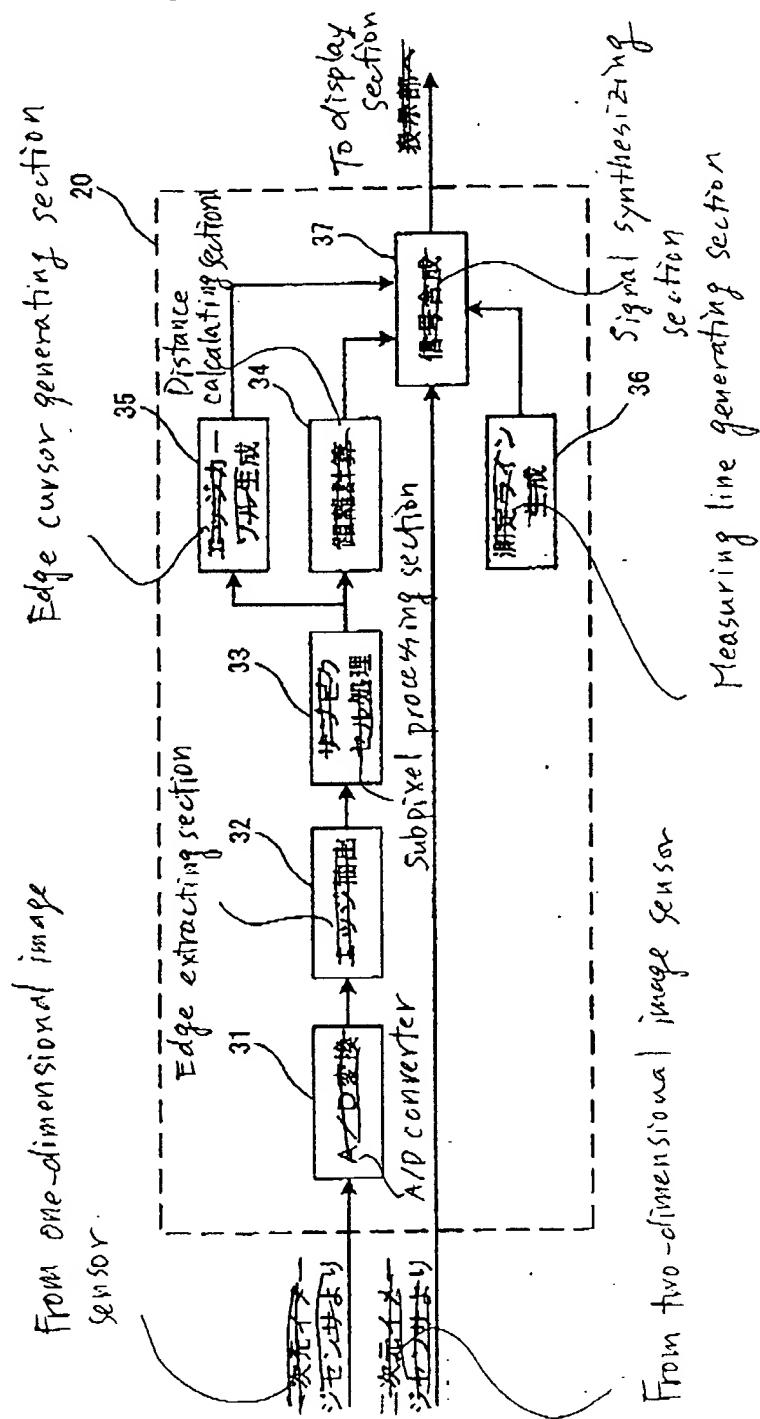
Light source driving circuit

【図2】



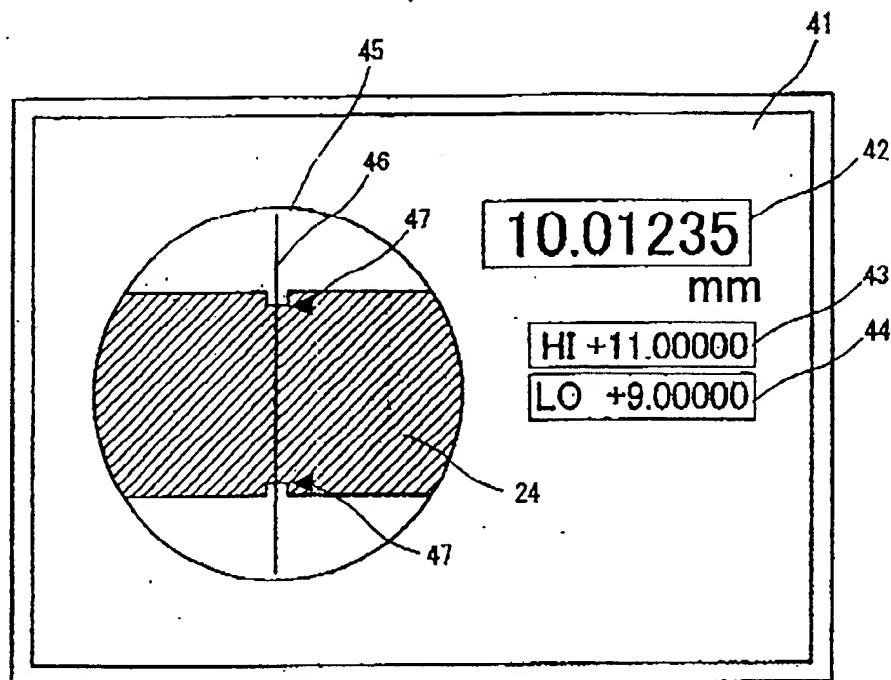
【図3】

Fig. 3



(図4)

Fig. 4



【図5】

Fig. 5A

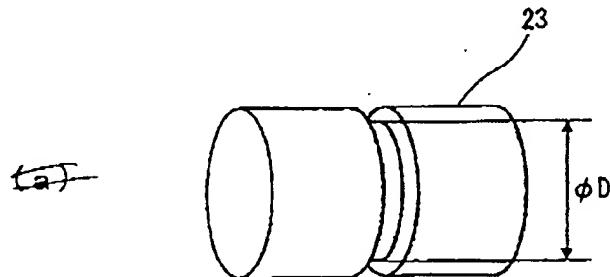


Fig. 5B

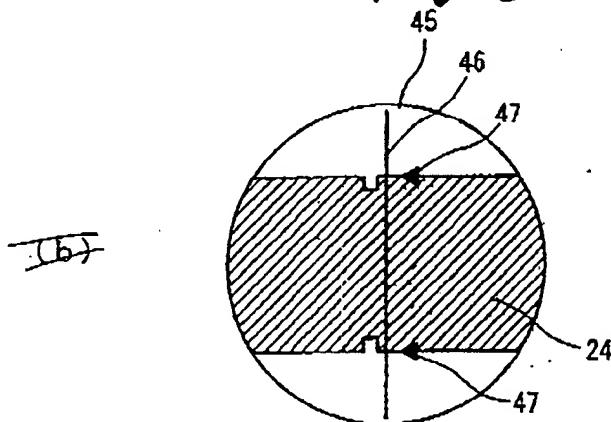
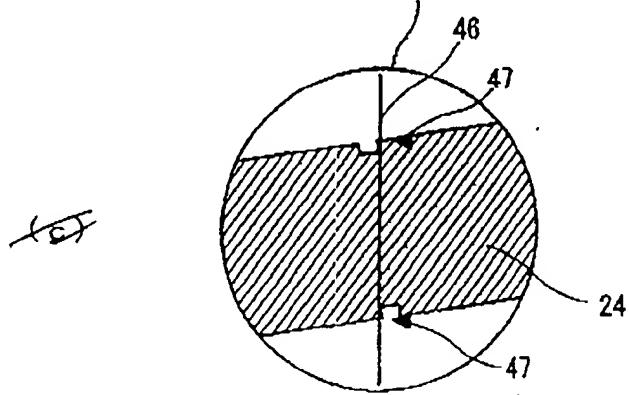
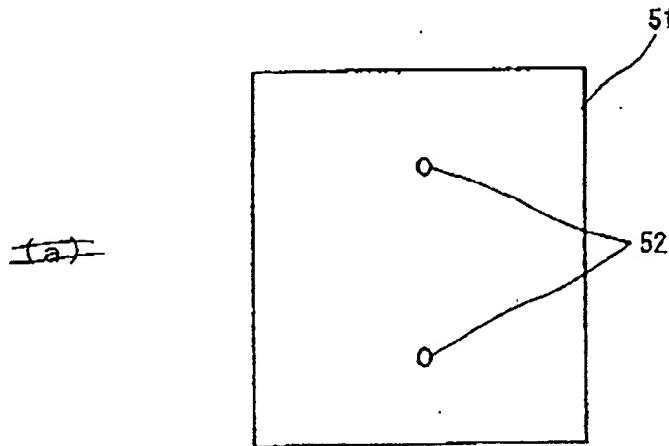


Fig. 5C



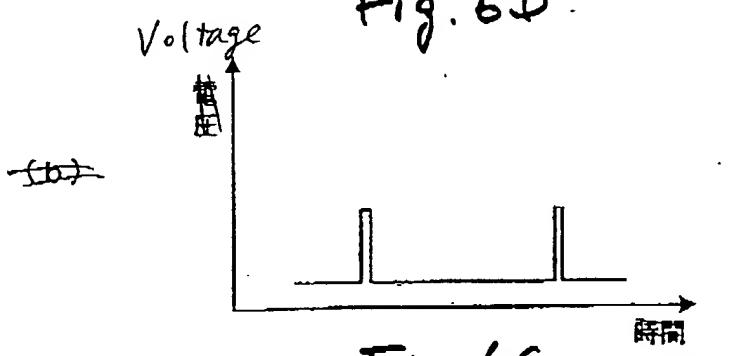
【図6】

Fig. 6 A



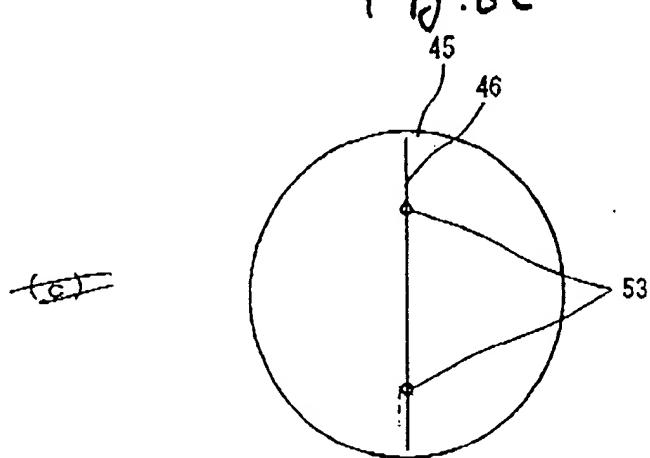
(a)

Fig. 6 B



(b)

Fig. 6 C



(c)

【図7】

